

INTERFERENCE

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L20	1	((hardware with emulat\$4) and ((scan near3 chain) or (shift near3 register) or (flip near3 flop)) and compar\$4 and mode).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/23 09:35

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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4049	test\$3 with emulat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/23 08:41
L2	177	(test\$3 with emulat\$3) same arrang\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/23 08:41
L3	77	(test\$3 with emulat\$3) same arrang\$5 and (flip-flop or register) and compar\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/23 08:41
L4	108	(test\$3 with emulat\$3) same arrang\$5 and (flip-flop or register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/23 08:41
L5	108	(test\$3 with emulat\$3) same arrang\$5 and (flip-flop or register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/23 08:41
L6	46	(test\$3 with emulat\$3) same arrang\$5 and (flip-flop or register) and compar\$4 and shift\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/23 08:41
L7	46	(test\$3 with emulat\$3) same arrang\$5 and (flip-flop or register) and compar\$4 and shift\$4 and emulat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/23 08:41
L8	46	(test\$3 with emulat\$3) same arrang\$5 and (flip-flop or register) and compar\$4 and shift\$4 and emulat\$3 and (emulat\$3 same test\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/23 08:41

L9	46	(test\$3 with emulat\$3) same arrang\$5 and (flip-flop or register) and compar\$4 and shift\$4 and emulat\$3 and (emulat\$3 same test\$4) and arrang\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/23 08:41
L10	1	(test\$3 with emulat\$3) same arrang\$5 and (flip-flop or register) and compar\$4 and shift\$4 and emulat\$3 and (emulat\$3 same test\$4) and arrang\$5 and (hdl or (hardware adj description adj language))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/23 08:41
L11	418	(hardware near4 emulat\$4) same test\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/23 08:41
L12	62	((hardware near4 emulat\$4) same test\$4) and compar\$4 and arrang\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/23 08:41
L13	50	((hardware near4 emulat\$4) same test\$4) and compar\$4 and arrang\$4 and (register or flip-flop)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/23 08:41
L14	32	hardware with emulat\$4 same (scan near3 chain or shift near3 register or flip near3 flop)	USPAT	OR	OFF	2005/12/23 09:13
L15	7	hardware with emulat\$4 same (scan near3 chain or shift near3 register or flip near3 flop) and "716"/\$.ccls.	USPAT	OR	OFF	2005/12/23 09:14
L16	93	hardware with emulat\$4 same (scan near3 chain or shift near3 register or flip near3 flop)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/23 09:32
L17	25	hardware with emulat\$4 same (scan near3 chain or shift near3 register or flip near3 flop) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/23 09:14